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Group Art Unit 2817 Atten: Examiner A. Kinkead (703) 872-9306

Docket No.: RFM-006 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.

: 10/605,635

Docket No. : RFM-006 US

Applicants

: Lazarescu et al.

Customer No.: 30499

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Filed Art Unit : Oct. 15, 2003

: 2817 Examiner : A. Kinkead Confirm No. : 2634

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Examiner Kinkead:

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No inference should be made that the information and references cited are "prior art" as defined in 35 USC §§ 102 or 103, and the Applicant reserves the right to establish that any of the cited references are not "prior art." No representation is being made that a search has been conducted, or that this statement encompasses all the possible relevant information.

Application No.: 10/605,635 Information Disclosure Statement PATENT Page 2 of 2

The Applicant believes that no fee is required for submission of this statement, since it is being submitted prior to the first Office Action.

Respectfully submitted,

Clifford B. Perry Attorney for Applicants

Reg. No. 43,854

449 Santa Fe Drive, #312 Encinitas, CA 92024 cperry@perryiplaw.com

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Sharing to Jour I-m. 10	Application Number	10/605,635		
INFORMATION DISCLOSURE	Filing Date	10/15/2003		
INFORMATION DISCLOSURE	First Named Inventor	Lazarescu		
STATEMENT BY APPLICANT	Art Unit	2817		
(Use as many sheets as necessary)	Examiner Name	A. Kinkead		
Sheet 1 of 1	Attorney Docket Number	RFM-006 US		

			U. S. PATEN	DOCUMENTS	
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Cade ^{2 (* soort)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		ZANNOTH, M. et. al, "A Single-Chip SI-Bipolar 1.6 GHz VCO with Integrated-Bias Network," IEEE Trans. MTT (NY), vol. 48, no. 2, Feb. 2000, pgs. 203-205.	
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		DERANTER, C. et. al. "High Data Rate Transmitter Circuits: RF CMOS Design and Tecniques for Design Automation," Kluwer Academic Publishers, ISBN 1402075456, pgs. 177-179	
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